

Matrix MatGraph C32
Art-Nr. T-C32-PT01010

256 KB RAM on Board:
HYUNDAI SEMICONDUCTOR
HY53C464-Series

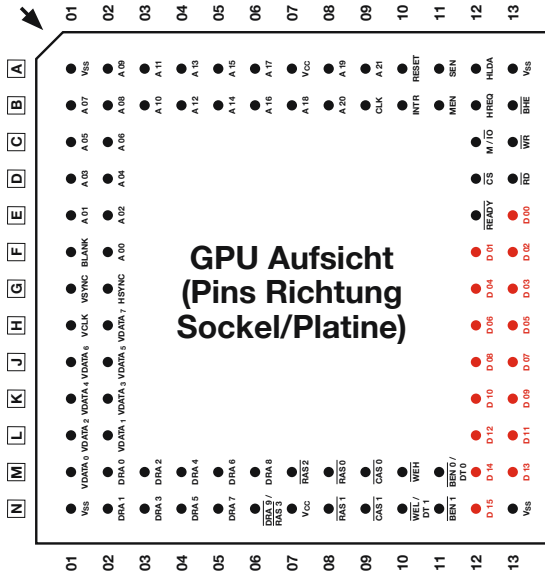
Durchgangsprüfung

Intel GPU 82786
Datensignale D00–D15

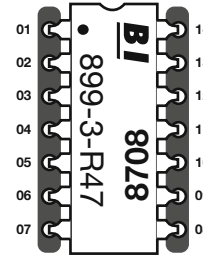
zu den

RAM-Bausteinen
Data Input / Output
I/O 0 – I/O 3

GPU-PIN	GPU-Bedeutung	RAM-Baustein	RAM-PIN	RAM-Bedeutung
E - 13	D 00	1 5	2	I/O 0
F - 12	D 01	1 5	3	I/O 1
F - 13	D 02	1 5	15	I/O 2
G - 13	D 03	1 5	17	I/O 3
G - 12	D 04	2 6	2	I/O 0
H - 13	D 05	2 6	3	I/O 1
H - 12	D 06	2 6	15	I/O 2
J - 13	D 07	2 6	17	I/O 3
J - 12	D 08	3 7	2	I/O 0
K - 13	D 09	3 7	3	I/O 1
K - 12	D 10	3 7	15	I/O 2
L - 13	D 11	3 7	17	I/O 3
L - 12	D 12	4 8	2	I/O 0
M - 13	D 13	4 8	3	I/O 1
M - 12	D 14	4 8	15	I/O 2
N - 12	D 15	4 8	17	I/O 3



GPU Aufsicht
(Pins Richtung
Socket/Platine)



Resistor
47 Ω

GPU-PIN	GPU-Bedeutung	RAM-Baustein	RAM-PIN	RAM-Bedeutung
M - 11	$\overline{\text{BEN 0}} / \text{DT 0}$	5 6 7 8	1 47 Ω	$\overline{\text{OE}}$
N - 11	$\overline{\text{BEN 1}}$	1 2 3 4	1 47 Ω	$\overline{\text{OE}}$
M - 09	$\overline{\text{CAS 0}}$	5 6 7 8	16 47 Ω	$\overline{\text{CAS}}$
N - 09	$\overline{\text{CAS 1}}$	1 2 3 4	16 47 Ω	$\overline{\text{CAS}}$
M - 08	$\overline{\text{RAS 0}}$	1 2 3 4 5 6 7 8	5 47 Ω	$\overline{\text{RAS}}$

