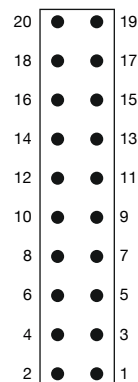


Matrix MatGraph C32
Art-Nr. T-C32-PT01010

256 KB RAM on Board:
HYUNDAI SEMICONDUCTOR
HY53C464-Series

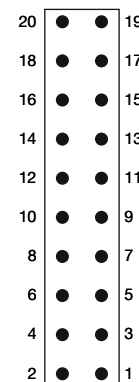
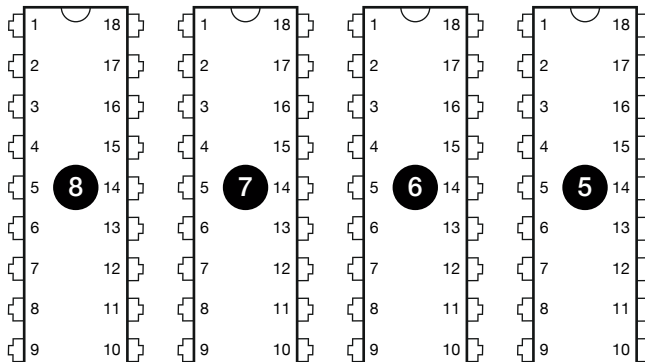
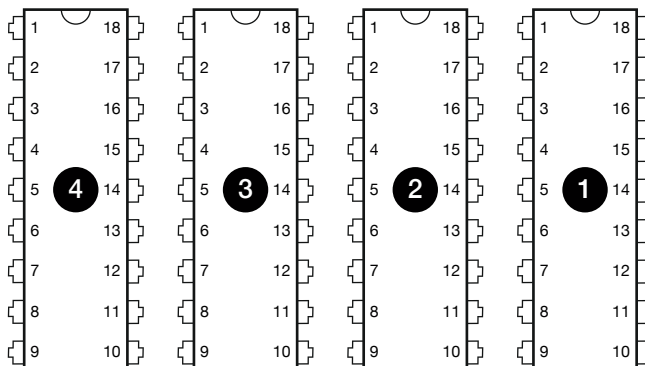
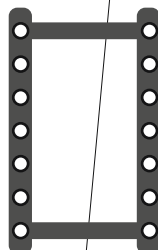
Durchgangsprüfung
2x Stiftleisten
(je 20 Pins)
zu
8x RAM-Bausteinen
(je 18 Pins)

Resistor entfernt →



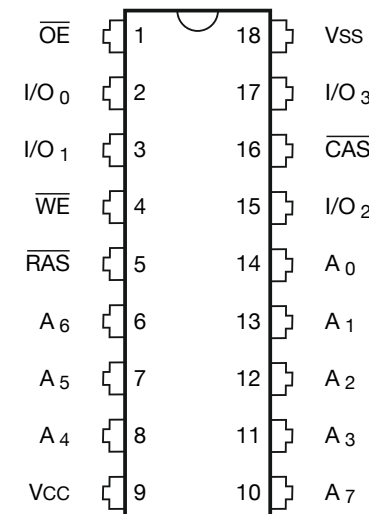
A

SV1



B

SV2



| | |
|-------------------------------------|-----------------------|
| \overline{OE} | Output Enable |
| I/O ₀ - I/O ₂ | Data Input / Output |
| \overline{WE} | Write Enable |
| \overline{RAS} | Row Address Strobe |
| A ₀ - A ₇ | Address Input |
| VCC | Power (+5V) |
| Vss | Ground |
| \overline{CAS} | Column Address Strobe |

| Stiftleiste - PIN | RAM-Baustein | RAM-PIN | Stiftleiste - PIN | RAM-Baustein | RAM-PIN |
|-------------------|-----------------|---------|-------------------|-----------------|---------|
| A - 20 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 14 | A - 19 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 13 |
| A - 18 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 12 | A - 17 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 11 |
| A - 16 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 8 | A - 15 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 7 |
| A - 14 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 6 | A - 13 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 10 |
| A - 12 | X | X | A - 11 | X | X |
| A - 10 | X | X | A - 9 | X | X |
| A - 8 | X | X | A - 7 | X | X |
| A - 6 | X | X | A - 5 | X | X |
| A - 4 | X | X | A - 3 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 18 |
| A - 2 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 18 | A - 1 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 18 |

| Stiftleiste - PIN | RAM-Baustein | RAM-PIN | Stiftleiste - PIN | RAM-Baustein | RAM-PIN |
|-------------------|-----------------|---------|-------------------|-----------------|---------|
| B - 20 | ① ⑤ | 2 | B - 19 | ① ⑤ | 3 |
| B - 18 | ① ⑤ | 15 | B - 17 | ① ⑤ | 17 |
| B - 16 | ② ⑥ | 2 | B - 15 | ② ⑥ | 3 |
| B - 14 | ② ⑥ | 15 | B - 13 | ② ⑥ | 17 |
| B - 12 | ③ ⑦ | 2 | B - 11 | ③ ⑦ | 3 |
| B - 10 | ③ ⑦ | 15 | B - 9 | ③ ⑦ | 17 |
| B - 8 | ④ ⑧ | 2 | B - 7 | ④ ⑧ | 3 |
| B - 6 | ④ ⑧ | 15 | B - 5 | ④ ⑧ | 17 |
| B - 4 | ③ ④ ⑦ ⑧ | 4 | B - 3 | ① ② ⑤ ⑥ | 4 |
| B - 2 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 9 | B - 1 | ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ | 9 |