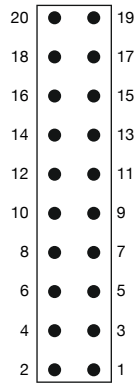
Stand: 26.10.2025

Matrix MatGraph C32
Art-Nr. T-C32-PT01010

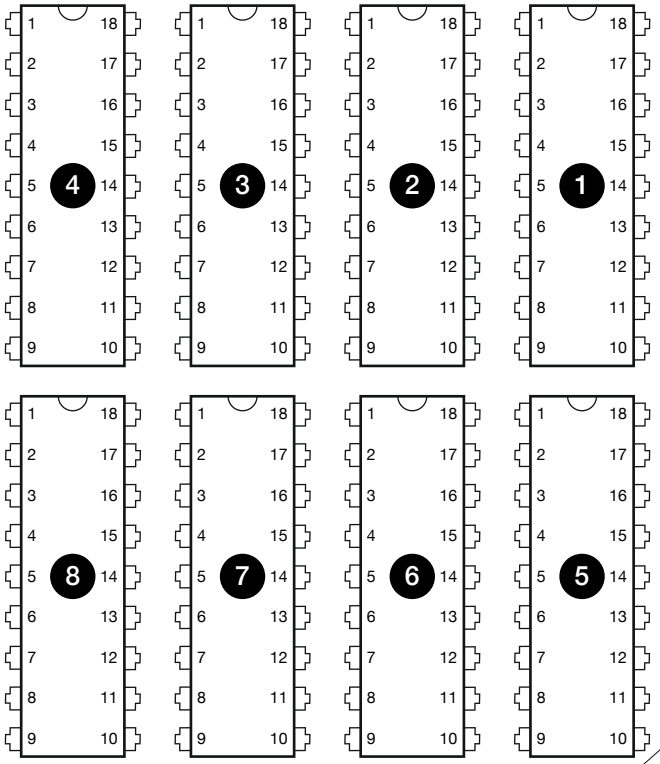
256 KB RAM on Board:
HYUNDAI SEMICONDUCTOR
HY53C464-Series

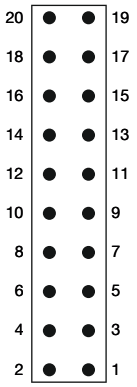
Durchgangsprüfung
2x Stiftleisten
(je 20 Pins)
zu
8x RAM-Bausteinen
(je 18 Pins)

Bei den PINs wurde direkter Durchgang gemessen, außer A-6, A-9, A-15 und A-16. Hier wurden Ohm-Werte ausgegeben ...



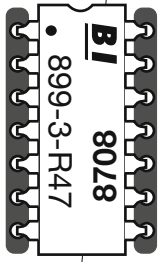
SV1

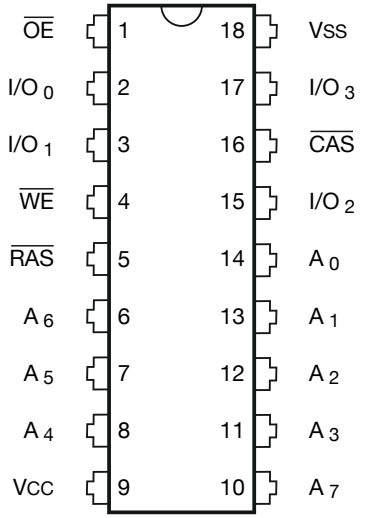




SV2

Resistor gesteckt 47 Ω





$\overline{OE}$	Output Enable
I/O 0 – I/O 2	Data Input / Output
$\overline{WE}$	Write Enable
$\overline{RAS}$	Row Adress Strobe
A 0 – A 7	Adress Input
Vcc	Power (+5V)
Vss	Ground
$\overline{CAS}$	Column Adress Strobe

Stiftleiste – PIN	RAM-Baustein	RAM-PIN
A – 20	1 2 3 4 5 6 7 8	14
A – 18	1 2 3 4 5 6 7 8	12
A – 16	1 2 3 4 5 6 7 8	8
A – 14	1 2 3 4 5 6 7 8	6
A – 12	X	X
A – 10	5 6 7 8	1 47 Ω
A – 8	X	X
A – 6	X	X
A – 4	5 6 7 8	16 47 Ω
A – 2	1 2 3 4 5 6 7 8	18

Stiftleiste – PIN	RAM-Baustein	RAM-PIN
A – 19	1 2 3 4 5 6 7 8	13
A – 17	1 2 3 4 5 6 7 8	11
A – 15	1 2 3 4 5 6 7 8	7
A – 13	1 2 3 4 5 6 7 8	10
A – 11	1 2 3 4	5 47 Ω
A – 9	1 2 3 4	16 47 Ω
A – 7	X	X
A – 5	1 2 3 4	1 47 Ω
A – 3	1 2 3 4 5 6 7 8	18
A – 1	1 2 3 4 5 6 7 8	18

Stiftleiste – PIN	RAM-Baustein	RAM-PIN
B – 20	1 5	2
B – 18	1 5	15
B – 16	2 6	2
B – 14	2 6	15
B – 12	3 7	2
B – 10	3 7	15
B – 8	4 8	2
B – 6	4 8	15
B – 4	3 4 7 8	4
B – 2	1 2 3 4 5 6 7 8	9

Stiftleiste – PIN	RAM-Baustein	RAM-PIN
B – 19	1 5	3
B – 17	1 5	17
B – 15	2 6	3
B – 13	2 6	17
B – 11	3 7	3
B – 9	3 7	17
B – 7	4 8	3
B – 5	4 8	17
B – 3	1 2 5 6	4
B – 1	1 2 3 4 5 6 7 8	9