

**Matrix MatGraph C32**  
**Art-Nr. T-C32-PT01010**

**256 KB RAM on Board:**  
**HYUNDAI SEMICONDUCTOR**  
**HY53C464-Series**

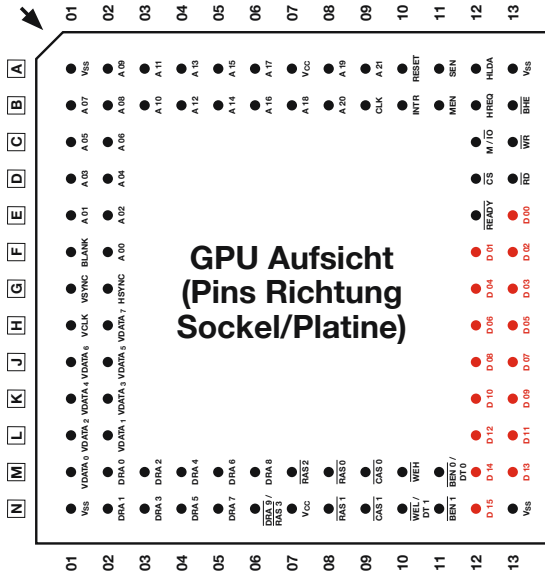
**Durchgangsprüfung**

**Intel GPU 82786**  
**Datensignale D00–D15**

zu den

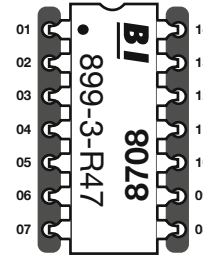
**RAM-Bausteinen**  
**Data Input / Output**  
**I/O 0 – I/O 3**

GPU-PIN	GPU-Bedeutung	RAM-Baustein	RAM-PIN	RAM-Bedeutung
<b>E</b> - 13	D 00	4 8	2	I/O 0
<b>F</b> - 12	D 01	4 8	3	I/O 1
<b>F</b> - 13	D 02	4 8	15	I/O 2
<b>G</b> - 13	D 03	4 8	17	I/O 3
<b>G</b> - 12	D 04	3 7	2	I/O 0
<b>H</b> - 13	D 05	3 7	3	I/O 1
<b>H</b> - 12	D 06	3 7	15	I/O 2
<b>J</b> - 13	D 07	3 7	17	I/O 3
<b>J</b> - 12	D 08	2 6	2	I/O 0
<b>K</b> - 13	D 09	2 6	3	I/O 1
<b>K</b> - 12	D 10	2 6	15	I/O 2
<b>L</b> - 13	D 11	2 6	17	I/O 3
<b>L</b> - 12	D 12	1 5	2	I/O 0
<b>M</b> - 13	D 13	1 5	3	I/O 1
<b>M</b> - 12	D 14	1 5	15	I/O 2
<b>N</b> - 12	D 15	1 5	17	I/O 3



**GPU Aufsicht**  
**(Pins Richtung**  
**Socket/Platine)**

**Resistor**



GPU-PIN	GPU-Bedeutung	RAM-Baustein	RAM-PIN	RAM-Bedeutung
<b>M</b> - 11	BEN 0 / DT 0	5 6 7 8	1 47,5 Ω	OE
<b>N</b> - 11	BEN 1	1 2 3 4	1 47,5 Ω	OE
<b>M</b> - 09	CAS 0	5 6 7 8	16 47,5 Ω	CAS
<b>N</b> - 09	CAS 1	1 2 3 4	16 47,5 Ω	CAS
<b>M</b> - 08	RAS 0	1 2 3 4 5 6 7 8	5 47,5 Ω	RAS

